IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Douglas J McKenney et al. : Date: 01 December 2006

Group Art Unit: 2825 : LSI Logic Corporation

Examiner: Stacy Whitmore : Intellectual Property Law

Serial No.: 10/713,492 : MS D-106

 Filed:
 14 November 2003
 : 1621 Barber Lane

 Confirmation No.:
 9210
 : Milpitas, CA 95035

Title: FLEXIBLE DESIGN FOR MEMORY USE IN INTEGRATED CIRCUITS

Mail Stop Amendment Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATE OF TRANSMISSION

I hereby certify that the enclosed or attached correspondence is being transmitted on the U.S. Patent Electronic Businss Center at the website: https://sportal.uspto.gov/secorue/myportal/privatepair to the Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA

22313-1450 on 01 December 2006

Karuna Cikhen Reg No. 32 48

AMENDMENT UNDER 37 CFR 1.111

INTRODUCTORY COMMENTS

In response to the Examiner's Action mailed 05 September 2006, with a shortened statutory period set to expire on 05 December 2006, Applicants submit the following amendments to the claims and remarks.

Amendments to the claims are reflected in the listing of claims beginning on page 2 of this paper.

Remarks are included on pages 9-11 of this paper.